

AMENDMENTS TO THE SPECIFICATION

Please replace the first paragraph on page 38 with the following amended paragraph:

With reference to Figure 17 as well as to Figure [[4]] 4A, the F0, F1 and F2 bits specify the F inputs 43 to continuous time block 40. The three bits F0, F1 and F2 in combination can be used to specify eight different states. Similarly, the three bits P0, P1 and P2 and the three bits N0, N1 and N2 are for specifying the P inputs 41 and N inputs 42.